

FIG. 1

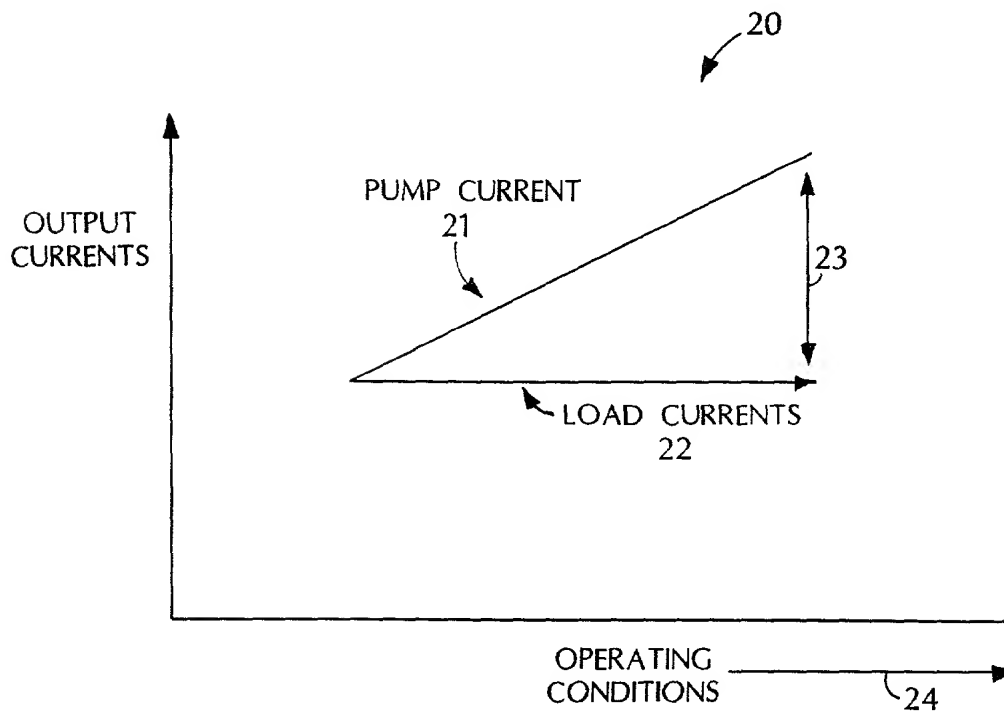


FIG. 2

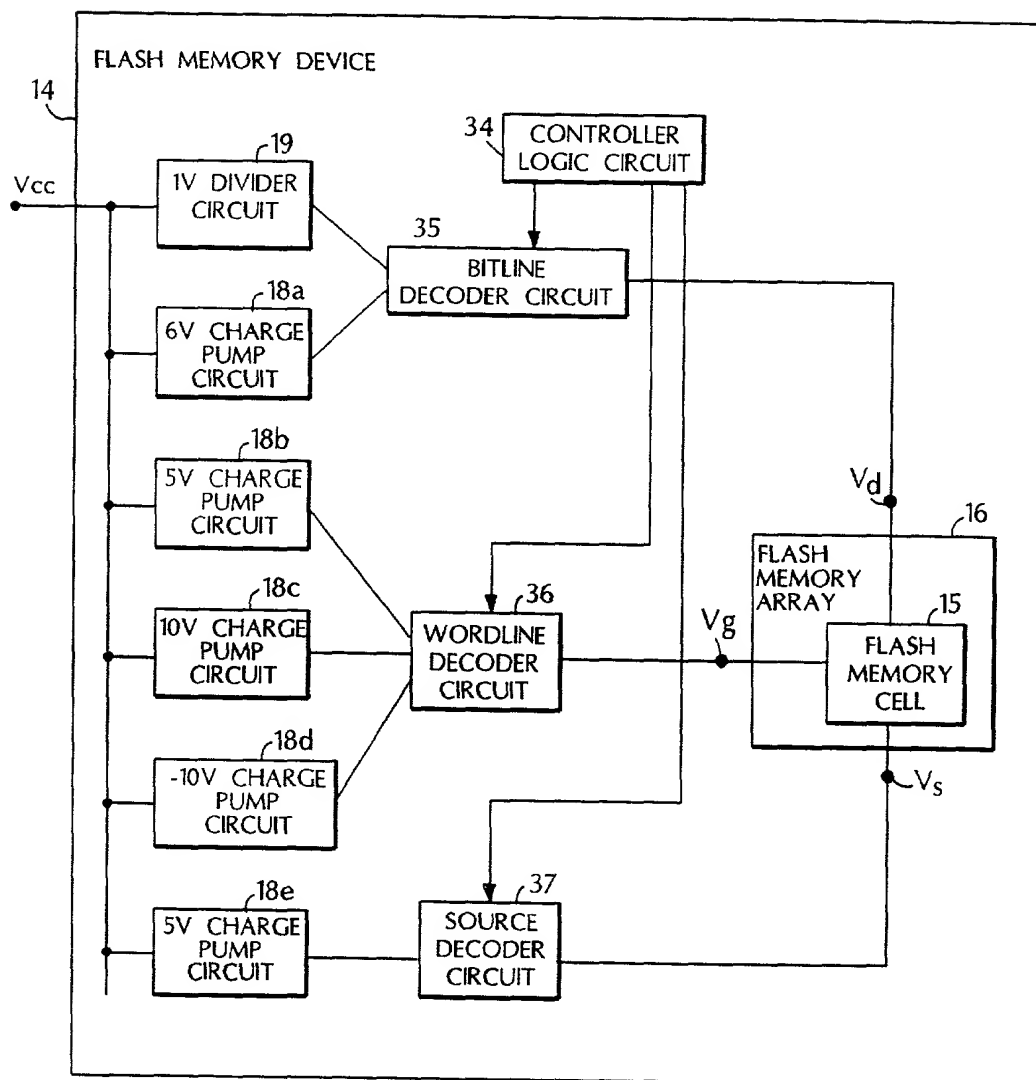


FIG. 3A

TABLE: VOLTAGE FOR PROCESSING FLASH DEVICE

| | <u>Vg</u> | <u>Vd</u> | <u>Vs</u> |
|----------------|-----------|-----------|-----------|
| <u>PROGRAM</u> | 10 | 6 | 0 |
| <u>ERASE</u> | -10 | 0 | FLOAT |
| <u>READ</u> | 5 | 1 | 0 |

FIG. 3B

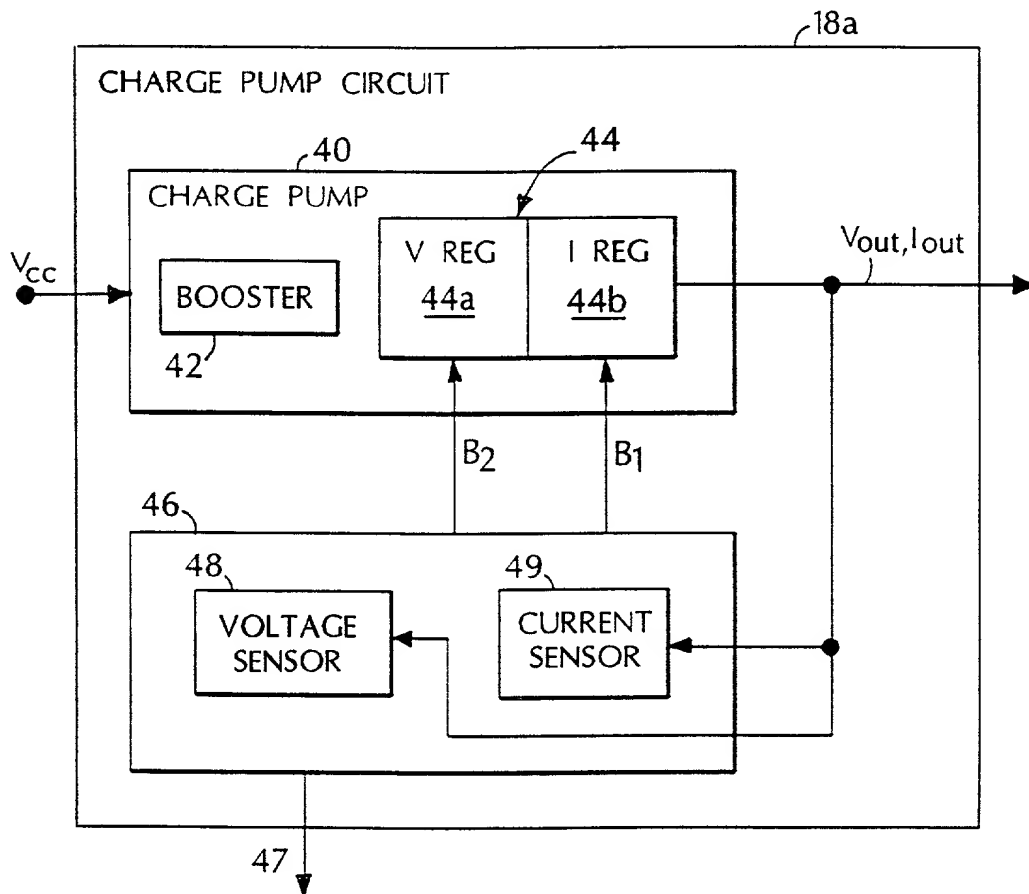


FIG. 4

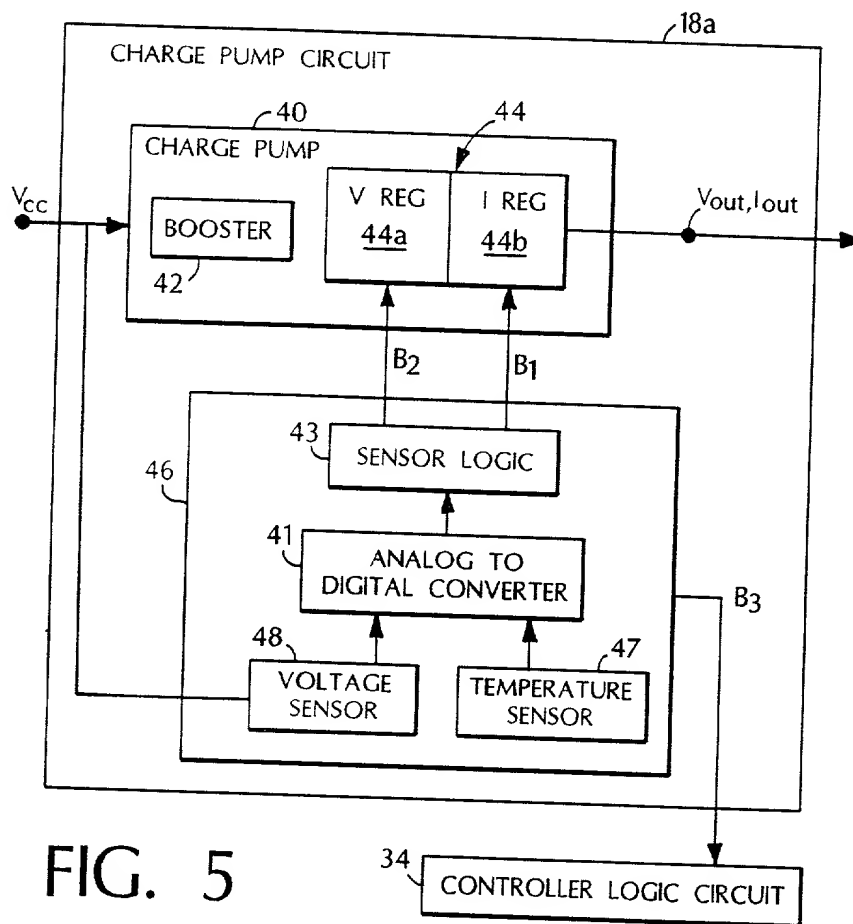


FIG. 5

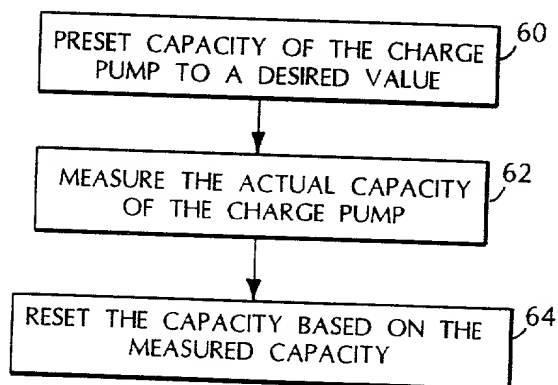


FIG. 6